

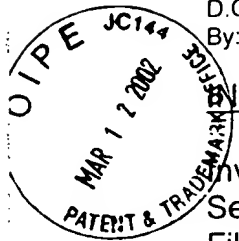
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Docket No.: GR 98 P 4137 P

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By: Markus Noll

Date: March 1, 2002

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THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Inventor : Hans-Jürgen Hacke et al.
Serial No. : 09/761,594
Filed : January 16, 2001
Title : Semiconductor Device in Chip Format and Method for
Producing It
Examiner : Kevin L. McHenry - Art Unit: 1725

RESPONSE

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

Sir:

Responsive to the Office Action dated February 19, 2002, the following remarks are made:

In deference to the restriction requirement on page 2 of the above-identified Office action, applicants elect group I, claims 1-8, for prosecution at this time.

Applicants reserve the right to file a divisional application including the method claims of group II at a later date and/or to request a rejoinder under MPEP §821.04.

In view of the foregoing, the early issuance of an Action on the merits of claims 1-8 is solicited.

Respectfully submitted,

Markus Noll

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